4800 Series Interval Timer, Fixed Timing, Solid State Output

Product Facts

- DC input fixed delay interval timer
- 1 Form A (SPST-NO). 500mA output
- CMOS digital design
- Reverse polarity protection
- Hermetic package
- Built to MIL-R-83726 environmentals
- Customizing options include
 - Adjustable timing
 - Tighter timing tolerances
 - Header and mounting
 - Relay output
 - AC input

Electrical Specifications

Timing Range: 100 s. to 600 s.

Tolerance: ±10%. Repeatability: ±2%

Recycle Time: 0.5% of Max. Delay

Input Data:

Input Voltage: 18 to 31Vdc. Current Drain: 40mA. max.

Output Data:

Output Form: 1 Form A (SPST-NO)

Output Rating: 500mA @ +25°C; 200mA @ +125°C

Saturation Voltage:

1.0V, 500mA (25°C).

Leakage:

10μA (125°C).

Environmental Specifications

Temperature Range:

-55°C to +85°C or -55°C to +125°C. Vibration: 20 G's, 10 - 2,000 Hz.

Shock: 50 G's, 11 ± 1ms duration. Insulation Resistance: 1.000 megohms, min., at 500Vdc.

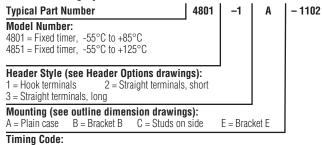
Dielectric Strength: 500Vrms, 60 Hz., at sea level, all terminals to case.

Sealing: Hermetic, 1.3 in. (33.0mm) of mercury.

Life: Over 1 million operations. Weight: 2 oz (50g) max.



Part Numbering System



Four-digit code for any value between 50ms and 600s.

The timing code consists of four digits and gives the time in ms. The first three digits are the significant figures and the last digit is the number of zeros following the significant figures; thus 50 ms would be coded 0500, 1.1 s would read 1101, and 1 m (60 s) would be 6002.

A typical part number would be 4801–1A–1102. This fixed timer operates at -55°C to +85°C, has hook terminals, style "A" mounting, and a time delay of 11s.

power to recycle.

KILOVAC 4800 series inter-

solid state outputs in robust

sures. They are fixed timers.

Apply power and the output will energize After time-out, the output will revert to

de-energized state. Remove and reapply

DELAY >

hermetically sealed enclo-

The 1 Form A (SPST-NO)

output switch is rated

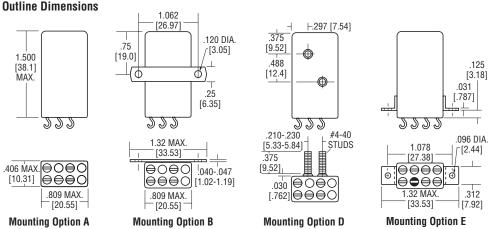
500mA.

OUTPUT

Timing Diagram

val timers combine solid

state timing circuits with



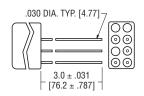
Wiring Diagram

LOAD

つ Φ 188 TYP

Header Options

.030 DIA. TYP. [4.77] $\overline{\cap}$ ⊚⊚ 00 00 188 TYP. [4.77]



TERMINAL SPACING IS 0.2 [5.08] FOR ALL HEADERS

Header Option 1

Header Option 2

Header Option 3

Plug-in sockets are available